

Docket No.: KUB-001



IFW  
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Kanji OTSUKA et al.

U.S. Patent Application No. 10/511, 720

Group Art Unit: 2631

Filed: October 18, 2004

Examiner:

For: HIGH-SPEED SIGNAL TRANSMISSION SYSTEM

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SUBMISSION OF INFORMATION DISCLOSURE STATEMENT

Sir:

Submitted herewith are Information Disclosure Statement, Search Report and nineteen references.

Please consider the information disclosure statement.

Respectfully submitted,

KANESAKA BERNER & PARTNERS

A handwritten signature in cursive script, appearing to read "Manabu Kanesaka".

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Date: June 19, 2006  
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<b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  <b>(PTO-1449)</b>				ATTY. DOCKET NO. <b>KUB-001</b>		U.S. PATENT APPLICATION NO. <b>10/511,720</b>	
				APPLICANT <b>Kanji OTSUKA et al.</b>			
				FILING DATE <b>October 18, 2004</b>		GROUP	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	08-242151 A	09/17/1996	Japan			X	
	2000-156627 A	06/06/2000	Japan			X	
	2001-211211	08/03/2001	Japan			X	
	11-284126	10/15/1999	Japan			X	
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	2001-210959	08/03/2001	Japan			X	
	08-288891	11/01/1996	Japan			X	
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	2002-124635	04/26/2002	Japan			X	
	WO97/50176 (JP11-511943)	12/31/1997	PCT			X	
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
	Masahiro Murakawa, Yuji Kasai, Eiichi Takahashi, Hidenori Sakanashi, Neil Marston, Kiryu Shogo and Tetsuya Higuchi, "Analog Evolvable Hardware and Its Applications," Technical Report of the Institute of Electronics, Information and Communication Engineers, Vol. 100, No. 89 (May 19, 2000), pp. 1-8 (AI2000-10)						
	Isamu Kajitani, Tsutomu Hoshino, Masahiro Murakawa and Tetsuya Higuchi, "Implementation of the Circuit for Determining the Neural Network Structure with GAs," Japanese Neural Network Society Journal, Vol. 5, No. 4 (December 5, 1998), pp. 145-153						
	Kenichi Itoh, "Science and Technology," Nikkan Kogyo Shimbun, Ltd., July 28, 1998, pp. 74-88						
	Eikichi Yamashita, Hideyuki Ohashi and Kazuhiko Atsuki, "Simple CAD Formulas of Edge-Compensated Microstrip Lines," 1989 IEEE MTT-S International Microwave Symposium Digest (13-15 June 1989), pp. 339-342						
EXAMINER				DATE CONSIDERED			

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.